

Electronic Acknowledgement Receipt

EFS ID:	2331507
Application Number:	09919372
International Application Number:	
Confirmation Number:	2525
Title of Invention:	Clock distribution circuits and methods of operating same that use multiple clock circuits connected by phase detector circuits to generate and synchronize local clock signals
First Named Inventor/Applicant Name:	Vadim Gutnik
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Filer:	David Moore/Joyce Paoli
Filer Authorized By:	David Moore
Attorney Docket Number:	5347-205
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for status of Application	5347-205.pdf	56343 06fad11a413ceca3df2c73b6267a2084a784047e	no	1

Warnings:

Information:	
Total Files Size (in bytes):	56343
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